

## VCXO-7050/7050L

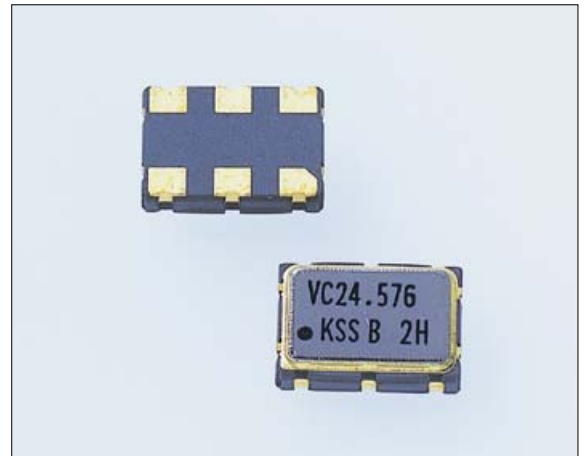
### Miniature VCXO Geared to Broadband

#### ■Features

- “Small Size” as small as 0.063cc. (5×7×1.8mm)
- Using 1 chip HCMOS IC.
- 3-state devices are available.
- APR (Absolute Pull Range) of up to ±50ppm or ±100ppm※2.

#### ■Typical Applications

- Digital switching system
- ATM SDH
- SONET
- xDSL



Original Size

#### ■Specifications

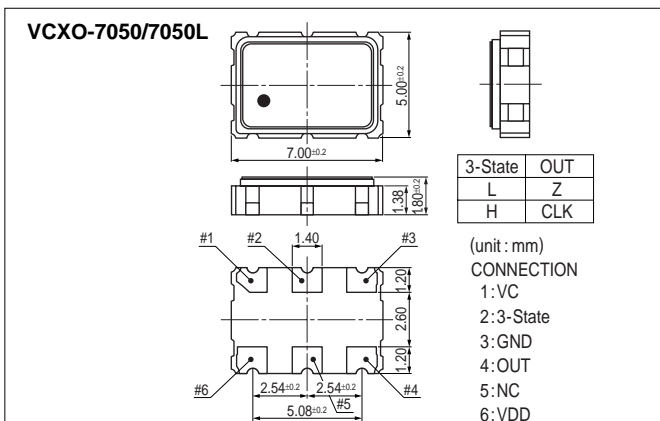
Item	Type	VCXO-7050	VCXO-7050L
周波数範囲 Frequency range		7.2000-80.0000MHz ※1	
APR (Absolute Pull Range)		±50×10 <sup>-6</sup> or ±100×10 <sup>-6</sup> ※2 (Vc=0.5-4.5V)	±50×10 <sup>-6</sup> or ±100×10 <sup>-6</sup> ※2 (Vc=0.3-3.0V)
Operable temperature range		-40~+85°C	
Storage temperature		-40~+90°C	
Aging		APR is tolerable with 20years aging	
Power supply voltage		V <sub>DD</sub> =+5.0V±10%	V <sub>DD</sub> =+3.3V±10%
Power supply current		15mA MAX.	
Output load		15pF±10%	
Output level		V <sub>OH</sub> : +0.9V <sub>DD</sub> MIN. / V <sub>OL</sub> : +0.1V <sub>DD</sub> MAX.	
Output symmetry		40~60% (at +2.50V DC)	40~60% (at +1.65V DC)
Rise time/Fall time		10ns MAX. / 10ns MAX.	
Vibration		MIL-STD-883 Method 2007 IEC68-2-6 test Fc MIL-STD-202-Method 204	
Shock		MIL-STD-883 Method 2002 IEC68-2-27 test Ea MIL-STD-202-Method 213	
Humidity		EIA / JESD22-A101	
Weight		0.17gram	

• APR=Absolute Pull Range is the minimum guaranteed (voltage controlled) frequency shift ( $\Delta f/f_{NOM}$ ) over all conditions (temperature, aging, power supply and load)

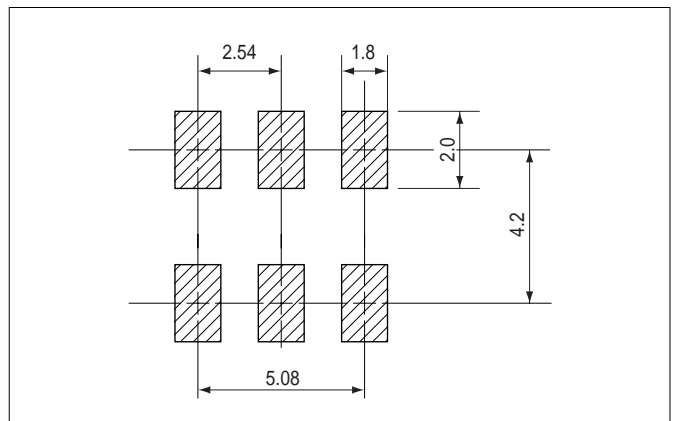
※1 33MHz to 80MHz are underdevelopment.

※2 For 100×10<sup>-6</sup>, ask our sales representative.

#### ■Outline



#### ■Land Pattern(reference)



Dimensions(mm)